

# Relay Power Control for In-Band Full-Duplex Decode-and-Forward Relay Networks Over Static and Time-Varying Channels

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**Abstract**—This article considers a one-way full-duplex decode-and-forward relay network consisting of a source, a relay, and a destination. Due to the self-interference (SI) channel estimation error at the relay, SI cannot be completely canceled. In the presence of the residual SI, there is a tradeoff between the achievable rate of the relay–destination link in a given time slot and that of the source–relay link in the next time slot depending on the relay transmit power in that time slot. As the relay transmit power increases, the achievable rate from the relay to destination increases, whereas the achievable rate from the source to the relay in the next time slot decreases. Motivated by this observation, relay power control schemes for maximizing achievable rates over static and time-varying channels are proposed in this article. Next, a closed-form expression for the relay transmit power in each time slot is derived. Numerical results show that the proposed relay power control schemes outperform the conventional maximum power transmission scheme in terms of achievable rates.

**Index Terms**—Achievable rate, decode-and-forward, full-duplex (FD), one-way relay, power control.

## I. INTRODUCTION

FULL-duplex (FD) transmission is envisaged as a key enabling technology for the next-generation wireless systems to improve spectral efficiency [1]–[5] because FD systems can transmit and receive simultaneously on the same frequency band. *In-band FD* technology allows a wireless terminal to transmit and receive simultaneously in the same frequency band, thus increasing the throughput of wireless communication networks. Specifically, FD relay systems have recently been studied in [6]–[15]. The beamforming design schemes were

proposed in [6] and [7], and the simultaneous wireless information and power transfer systems were studied in [8]–[11]. Also, the power allocation and control schemes for FD relay systems in the presence of the residual *self-interference* (SI) were investigated in [12]–[15]. FD systems come with SI, which degrades the system performance, if not treated. In particular, SI cannot be wholly canceled in an FD relay due to the SI channel estimation error at the relay and the limited dynamic range of the analog-to-digital converters [1]–[3]. For this reason, exploring techniques for mitigating SI performance degradation is essential.

Buffer-aided relay networks have been studied in [16]–[18]. Razlighi and Zlatanov [16] proposed buffer-aided relaying schemes with adaptive reception/transmission for the two-hop FD relay channel with SI when the source and the relay both perform continuous-rate transmission with adaptive-power allocation, continuous-rate transmission with fixed-power allocation, and discrete-rate transmission, respectively. In [17], source and relay power allocation for the buffer-aided FD relaying networks was studied, assuming constant data rate arrivals at the source buffer. Zlatanov *et al.* [18] studied the capacity of the Gaussian two-hop FD relay channel with linear residual SI considering the worst-case linear residual SI model. Also, the authors presented that the capacity is achieved by a zero-mean Gaussian input distribution at the source whose variance depends on the amplitude of the transmit symbols at the relay.

The FD system and low-latency communication are key technologies of fifth-generation (5G) wireless communication. In view of this trend, this article considers the FD relay-assisted transmissions for low-latency communication. The achievable data rate of buffer-aided relay networks can be greater than that of buffer-free relay networks; however, buffer-aided relay networks are considered mainly in delay-tolerant networks and require memory. For those reasons, this article considers buffer-free FD relay networks.

In one-way FD decode-and-forward relay networks, the signal from the source is transmitted to the relay in a time slot, and the decoded signal at the relay is transmitted to the destination in the next time slot. Therefore, the achievable rate from the source to the destination in a given time slot is determined by the minimum value of the achievable rate from the source to the relay in the previous time slot and the achievable rate from the relay to the destination in that time slot.

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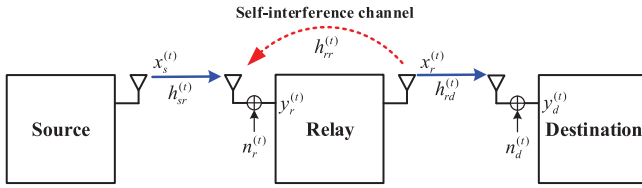


Fig. 1. One-way FD decode-and-forward relay networks in time slot  $t$ .

For these reasons, as the relay transmit power in a time slot increases, the achievable rate from the relay to the destination in the corresponding time slot increases, whereas the achievable rate from the source to the relay in the next time slot decreases due to the residual SI at the relay. This implies that there exists a tradeoff between the achievable rate from the relay to the destination in a time slot and the achievable rate from the source to the relay in the next time slot depending on the relay transmit power in that time slot. Motivated by this observation, this article studies the relay power control scheme for maximizing the achievable rate in each time slot.

Dun *et al.* [12] proposed power allocation schemes for the base station (BS) and relay in the FD amplify-and-forward relay-aided device-to-device communication. The optimal power allocation for two-way FD amplify-and-forward relay networks was studied in [13]. The power control schemes for the FD relay networks were proposed in [14] and [15]. Li *et al.* [14] proposed the relay power control scheme for two-way FD amplify-and-forward relay networks in each time slot over static channels. The FD decode-and-forward relay-enhanced cellular networks consisting of an FD BS, two FD relays, and two half-duplex user equipments (UEs) are studied in [15]. This study proposed a power control scheme for the BS, relays, and UEs that maximize the achievable end-to-end spectral efficiency of the uplink and downlink UEs in a certain time slot. To the best of our knowledge, the relay power control schemes in each time slot for one-way FD decode-and-forward relay networks over static and time-varying channels have not been proposed in the literature.

The remainder of this article is organized as follows. Section II describes the system model. Sections III and VI provide the proposed relay power control schemes over static and time-varying channels, respectively. Section V shows the numerical results for the proposed relay power control schemes. Finally, Section VI concludes this article.

*Notations:* The operator  $\mathbb{E}[\cdot]$  indicates the expectation. Notations  $|a|$  denotes the absolute value of  $a$  for any scalar. Notations  $\max(\cdot)$  and  $\min(\cdot)$  denote the largest value of the arguments and the smallest value of the arguments, respectively.

## II. SYSTEM MODEL

Consider a one-way FD *decode-and-forward* relay network consisting of one source node  $s$ , one relay node  $r$ , and one destination node  $d$ , as shown in Fig. 1. Since the relay is assumed to operate in the FD mode, there is the SI channel. The *channel state information* (CSI) of SI is assumed to be imperfect due to channel estimation errors.

In time slot 0, the source transmits its signal  $x_s^{(0)}$  to the relay. The received signal at the relay is written as

$$y_r^{(0)} = h_{sr}^{(0)} x_s^{(0)} + n_r^{(0)} \quad (1)$$

where  $h_{sr}^{(0)}$  is the channel coefficient between the source and relay in time slot 0. Also,  $n_r^{(0)}$  is the additive white Gaussian noise (AWGN) at the relay in time slot 0 whose mean is zero and variance is  $\sigma_{n,r}^2$ .

In time slot 1, the relay forwards the decoded signal to the destination and the source transmits its next signal to the relay at the same time. In this manner, the source transmits its signal to the relay and the relay transmits the decoded signal to the destination in each time slot. In time slot  $t-1$ , the source transmits the signal  $x_s^{(t-1)}$  using maximum transmit power  $p_s$ , i.e.,  $\mathbb{E}[|x_s^{(t-1)}|^2] = p_s$ . Also, the relay transmits the signal  $x_r^{(t-1)}$  using transmit power  $p_r^{(t-1)}$  with  $\mathbb{E}[|x_r^{(t-1)}|^2] = p_r^{(t-1)}$  ( $0 < p_r^{(t-1)} \leq p_r^{\max}$ ). The estimated received signal at the relay after subtracting SI in time slot  $t-1$  is written as

$$\begin{aligned} \hat{y}_r^{(t-1)} &= y_r^{(t-1)} - \hat{h}_{rr}^{(t-1)} x_r^{(t-1)} \\ &= h_{sr}^{(t-1)} x_s^{(t-1)} + h_{rr}^{(t-1)} x_r^{(t-1)} + n_r^{(t-1)} - \hat{h}_{rr}^{(t-1)} x_r^{(t-1)} \\ &= h_{sr}^{(t-1)} x_s^{(t-1)} + \Delta_{rr}^{(t-1)} x_r^{(t-1)} + n_r^{(t-1)} \end{aligned} \quad (2)$$

where  $h_{rr}^{(t-1)}$ ,  $\hat{h}_{rr}^{(t-1)}$ , and  $\Delta_{rr}^{(t-1)}$  are the true SI channel coefficient, the estimated SI channel coefficient, and the SI channel estimation error in time slot  $t-1$ , respectively. The mean and the variance of the SI channel estimation error are zero and  $\sigma_e^2$ , respectively. Also,  $h_{sr}^{(t-1)}$  is the channel coefficient between the source and the relay in time slot  $t-1$ . In addition,  $n_r^{(t-1)}$  is the AWGN at the relay in time slot  $t-1$ , and it has zero mean whose variance is  $\sigma_{n,r}^2$ .

The received signal at the destination in time slot  $t$  is written as

$$y_d^{(t)} = h_{rd}^{(t)} x_r^{(t)} + n_d^{(t)} \quad (3)$$

where  $h_{rd}^{(t)}$  is the channel coefficient between the relay and the destination in time slot  $t$ . In addition,  $n_d^{(t)}$  is the AWGN at the destination in time slot  $t$ , and it has zero mean whose variance is  $\sigma_{n,d}^2$ .

The achievable rate from the source to relay in time slot  $t-1$  is given by

$$\begin{aligned} R_{sr}^{(0)} &= \log_2 \left( 1 + \frac{p_s^{(0)} |h_{sr}^{(0)}|^2}{\sigma_{n,r}^2} \right) \\ R_{sr}^{(t-1)} &= \log_2 \left( 1 + \frac{p_s^{(t-1)} |h_{sr}^{(t-1)}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(t-1)}} \right) \quad \text{for } t \geq 2. \end{aligned} \quad (4)$$

Note that the relay is not affected by the residual SI in time slot 0 because the source only transmits the signal to the relay. The achievable rate from the relay to destination in time slot  $t$  is given by

$$R_{rd}^{(t)} = \log_2 \left( 1 + \frac{p_r^{(t)} |h_{rd}^{(t)}|^2}{\sigma_{n,d}^2} \right) \quad \text{for } t \geq 1. \quad (5)$$

In FD decode-and-forward relay networks, the achievable rate from the source to the destination in time slot  $t$  is determined by the minimum value of  $R_{sr}^{(t-1)}$  and  $R_{rd}^{(t)}$  [15], [19]. Therefore,  $R^{(t)}$  is given by

$$R^{(t)} = \min(R_{sr}^{(t-1)}, R_{rd}^{(t)}) \quad \text{for } t \geq 1. \quad (6)$$

It follows from (4) and (5) that as  $p_r^{(t)}$  increases,  $R_{rd}^{(t)}$  increases; however,  $R_{sr}^{(t)}$  decreases due to the residual SI.

### III. PROPOSED RELAY POWER CONTROL SCHEME OVER STATIC CHANNELS

This section introduces a relay power control scheme provided that CSI in each time slot is fixed. Therefore, the relay knows the CSI in all time slots. We consider the minimum achievable rate among the achievable rate in each time slot for guaranteeing the quality of service. When one frame consists of  $T$  time slots and the relay transmits the decoded signal to the destination until time slot  $T$ , the optimization problem for maximizing the minimum achievable rate can be written as

$$\text{(P1.1)} \quad \underset{p_r^{(1)}, \dots, p_r^{(T)}}{\text{maximize}} \quad \min\{R^{(1)}, \dots, R^{(T)}\} \quad (7a)$$

$$\text{subject to} \quad 0 < p_r^{(1)}, \dots, p_r^{(T)} \leq p_r^{\max} \quad (7b)$$

where  $T$  is the number of time slots in one frame. In problem (P1.1), the minimum achievable rate can be obtained by Lemma 1.

*Lemma 1:* The minimum achievable rate  $\min\{R^{(1)}, \dots, R^{(T)}\}$  is equivalent to

$$\min \left( \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_{\max}}, \frac{p_{\min} |h_{rd}|^2}{\sigma_{n,d}^2} \right) \quad (8)$$

where  $p_{\max} = \max(p_r^{(1)}, \dots, p_r^{(T-1)})$  and  $p_{\min} = \min(p_r^{(1)}, \dots, p_r^{(T-1)})$ .

*Proof:* The logarithm is an increasing function and  $\min\{\min(c_1, c_2), \dots, \min(c_{2T-1}, c_{2T})\}$  is equivalent to  $\min(c_1, c_2, \dots, c_{2T-1}, c_{2T})$ . Therefore,  $\min\{R^{(1)}, \dots, R^{(T)}\}$  can be simply rewritten as

$$R_{\min} = \min \left\{ \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2}, \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(1)}}, \dots, \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(T-1)}}, \right. \\ \left. \times \frac{p_r^{(1)} |h_{rd}|^2}{\sigma_{n,d}^2}, \dots, \frac{p_r^{(T-1)} |h_{rd}|^2}{\sigma_{n,d}^2}, \frac{p_r^{(T)} |h_{rd}|^2}{\sigma_{n,d}^2} \right\} \quad (9)$$

where  $p_s^{(0)} = \dots = p_s^{(T-1)} = p_s$ ,  $|h_{sr}^{(0)}|^2 = \dots = |h_{sr}^{(T-1)}|^2 = |h_{sr}|^2$ , and  $|h_{rd}^{(1)}|^2 = \dots = |h_{rd}^{(T)}|^2 = |h_{rd}|^2$  by assumption. Since the source transmits until time slot  $T-1$  and the relay transmits until time slot  $T$ ,  $p_r^{(T)}$  is set to  $p_r^{\max}$ . Due to  $p_r^{(T)} = p_r^{\max}$ ,  $\frac{p_r^{(T)} |h_{rd}|^2}{\sigma_{n,d}^2}$  is greater than or equal to  $\frac{p_r^{(t)} |h_{rd}|^2}{\sigma_{n,d}^2}$  for  $1 \leq t \leq T-1$ . In addition,  $\frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2}$  is greater than  $\frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(t)}}$  for  $1 \leq t \leq T-1$  unless  $\sigma_e^2$  is zero, i.e., when there is no residual SI. Therefore,  $R_{\min}$  can be written as

$$R_{\min} = \min(a_1, a_2) \quad (10)$$

where  $a_1$  and  $a_2$  are given by

$$a_1 = \min \left( \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(1)}}, \dots, \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(T-1)}} \right) \\ a_2 = \min \left( \frac{p_r^{(1)} |h_{rd}|^2}{\sigma_{n,d}^2}, \dots, \frac{p_r^{(T-1)} |h_{rd}|^2}{\sigma_{n,d}^2} \right). \quad (11)$$

In (11),  $a_1$  and  $a_2$  can be rewritten as  $f_1(p_{\max})$  and  $f_2(p_{\min})$ , respectively

$$f_1(p_{\max}) = \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_{\max}} \\ f_2(p_{\min}) = \frac{p_{\min} |h_{rd}|^2}{\sigma_{n,d}^2} \quad (12)$$

where  $p_{\max} = \max(p_r^{(1)}, \dots, p_r^{(T-1)})$  and  $p_{\min} = \min(p_r^{(1)}, \dots, p_r^{(T-1)})$ . ■

By Lemma 1, the problem (P1.1) can be rewritten as

$$\text{(P1.2)} \quad \underset{p_{\max}, p_{\min}}{\text{maximize}} \quad R_{\min}(p_{\max}, p_{\min}) \\ = \min\{f_1(p_{\max}), f_2(p_{\min})\} \\ \text{subject to} \quad 0 < p_{\max}, p_{\min} \leq p_r^{\max}. \quad (13)$$

*Lemma 2:* The minimum achievable rate  $R_{\min}(p_{\max}, p_{\min}) = \min\{f_1(p_{\max}), f_2(p_{\min})\}$  is maximized when  $p_{\max} = p_{\min}$ .

*Proof:* By definition,  $p_{\max}$  is greater than or equal to  $p_{\min}$ . Let  $p_{\max}^*$  and  $p_{\min}^*$  be the optimal solution for  $p_{\max}$  and  $p_{\min}$  that maximizes  $\min\{f_1(p_{\max}), f_2(p_{\min})\}$ . First of all, we consider  $p_{\max} > p_{\min}$ . Suppose  $p_{\max}^* > p_{\min}^*$ . We can divide the problem into three cases depending on the relation between  $f_1(p_{\max}^*)$  and  $f_2(p_{\min}^*)$ .

- 1) *Case*  $f_1(p_{\max}^*) < f_2(p_{\min}^*)$ : In this case,  $R_{\min}(p_{\max}^*, p_{\min}^*) = f_1(p_{\max}^*)$ . We consider the case in which  $p_{\max}^*$  is decreased by  $\epsilon$ , where  $\epsilon$  is a small positive value such that satisfy  $f_1(p_{\max}^* - \epsilon) < f_2(p_{\min}^*)$ . When  $p_{\max}^*$  is decreased by  $\epsilon$ ,  $R_{\min}(p_{\max}^* - \epsilon, p_{\min}^*) > R_{\min}(p_{\max}^*, p_{\min}^*)$  because of  $f_1(p_{\max}^* - \epsilon) > f_1(p_{\max}^*)$ . In other words, there exists  $p_{\max}$  and  $p_{\min}$  that satisfy  $R_{\min}(p_{\max}, p_{\min}) > R_{\min}(p_{\max}^*, p_{\min}^*)$ . Therefore, this case is a contradiction to the supposition that  $p_{\max}^*$  and  $p_{\min}^*$  are optimal.
- 2) *Case*  $f_1(p_{\max}^*) > f_2(p_{\min}^*)$ : In this case,  $R_{\min}(p_{\max}^*, p_{\min}^*) = f_2(p_{\min}^*)$ . We consider the case in which  $p_{\min}^*$  is increased by  $\epsilon$ , where  $\epsilon$  is a very small positive value such that satisfy  $f_1(p_{\max}^*) > f_2(p_{\min}^* + \epsilon)$ . When  $p_{\min}^*$  is increased by  $\epsilon$ ,  $R_{\min}(p_{\max}^*, p_{\min}^* + \epsilon) > R_{\min}(p_{\max}^*, p_{\min}^*)$  because of  $f_2(p_{\min}^* + \epsilon) > f_2(p_{\min}^*)$ . In other words, there exists  $p_{\max}$  and  $p_{\min}$  that satisfy  $R_{\min}(p_{\max}, p_{\min}) > R_{\min}(p_{\max}^*, p_{\min}^*)$ . Therefore, this case is a contradiction to the supposition that  $p_{\max}^*$  and  $p_{\min}^*$  are optimal.
- 3) *Case*  $f_1(p_{\max}^*) = f_2(p_{\min}^*)$ : We consider this case because we have identified two cases. In this case,  $R_{\min}(p_{\max}^*, p_{\min}^*) = f_1(p_{\max}^*) = f_2(p_{\min}^*)$ . We consider

the case in which  $p_{\max}^*$  is decreased by  $\epsilon_1$  and  $p_{\min}^*$  is increased by  $\epsilon_2$ , where  $\epsilon_1$  and  $\epsilon_2$  are very small positive values such that satisfy  $f_1(p_{\max}^* - \epsilon_1) = f_2(p_{\min}^* + \epsilon_2)$ . When  $p_{\max}^*$  is decreased by  $\epsilon_1$  and  $p_{\min}^*$  is increased by  $\epsilon_2$ ,  $R_{\min}(p_{\max}^* - \epsilon_1, p_{\min}^* + \epsilon_2) > R_{\min}(p_{\max}^*, p_{\min}^*)$  because of  $f_1(p_{\max}^* - \epsilon_1) = f_2(p_{\min}^* + \epsilon_2) > f_1(p_{\max}^*) = f_2(p_{\min}^*)$ . In other words, there exists  $p_{\max}$  and  $p_{\min}$  that satisfy  $R_{\min}(p_{\max}, p_{\min}) > R_{\min}(p_{\max}^*, p_{\min}^*)$ . Therefore, this case is a contradiction to the supposition that  $p_{\max}^*$  and  $p_{\min}^*$  are optimal.

To sum up,  $p_{\max}^*$  should not be greater than  $p_{\min}^*$ . Therefore, there exists the optimal  $p_{\max}^*$  and  $p_{\min}^*$  for maximizing  $R_{\min}(p_{\max}, p_{\min})$  when  $p_{\max}^* = p_{\min}^*$ . ■

By Lemma 2, we consider the case that  $p_{\max}^*$  and  $p_{\min}^*$  are the same. For  $p_{\max}^*$  and  $p_{\min}^*$  to be equal, the relay transmit power in each time slot should be the same because  $p_{\max}^*$  and  $p_{\min}^*$  are the maximum and minimum values of the relay transmit power value in each time slot, respectively. We define  $p_r$  as  $p_{\max}^* = p_{\min}^*$  and problem (P1.2) can be rewritten as

$$(P1.3) \quad p_r^* = \arg \max_{p_r} \min\{f_1(p_r), f_2(p_r)\} \quad (14a)$$

$$\text{subject to } 0 < p_r \leq p_r^{\max} \quad (14b)$$

where  $f_1(p_r)$  and  $f_2(p_r)$  are given by

$$\begin{aligned} f_1(p_r) &= \frac{p_s |h_{sr}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r} \\ f_2(p_r) &= \frac{p_r |h_{rd}|^2}{\sigma_{n,d}^2}. \end{aligned} \quad (15)$$

*Theorem 1:* The optimal relay transmit power  $p_r^{*(t)}$  for maximizing the minimum achievable rate  $\min\{f_1(p_r), f_2(p_r)\}$  is  $\min(\alpha, p_r^{\max})$  for  $1 \leq t \leq T-1$  and  $p_r^{\max}$  for  $t = T$ , where

$$\alpha = \sqrt{\left(\frac{\sigma_{n,r}^2}{2\sigma_e^2}\right)^2 + \frac{\sigma_{n,d}^2 p_s |h_{sr}|^2}{\sigma_e^2 |h_{rd}|^2}} - \frac{\sigma_{n,r}^2}{2\sigma_e^2}. \quad (16)$$

*Proof:* It is noted that  $f_1(p_r)$  is a strictly decreasing function and  $f_2(p_r)$  is a strictly increasing function with respect to  $p_r$ . In addition,  $f_1(0)$  is greater than  $f_2(0)$ . Therefore,  $f_1(p_r)$  and  $f_2(p_r)$  meet at one point if we ignore the constraint, which is defined as  $\alpha$ . By solving  $f_1(p_r) = f_2(p_r)$  using the quadratic formula,  $\alpha$  can be obtained.

Two cases are considered depending on the relation between  $\alpha$  and  $p_r^{\max}$ . Fig. 2 shows possible cases. We determine  $p_r$  for maximizing  $\min\{f_1(p_r), f_2(p_r)\}$ .

1) *Case I* ( $0 < \alpha \leq p_r^{\max}$ ): As shown in Fig. 2(a),  $\min\{f_1(p_r), f_2(p_r)\}$  can be written as

$$\min\{f_1(p_r), f_2(p_r)\} = \begin{cases} f_2(p_r), & \text{for } 0 < p_r \leq \alpha \\ f_1(p_r), & \text{for } \alpha \leq p_r \leq p_r^{\max}. \end{cases} \quad (17)$$

According to (17),  $\min\{f_1(p_r), f_2(p_r)\}$  is strictly increasing on  $(0, \alpha]$  and strictly decreasing on  $[\alpha, p_r^{\max}]$ . Therefore,  $p_r$  for maximizing  $\min\{f_1(p_r), f_2(p_r)\}$  is  $\alpha$ .

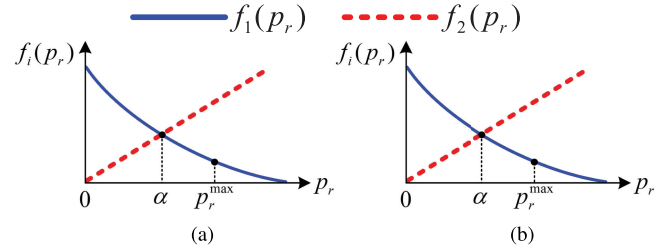


Fig. 2. Possible cases depending on the relation between  $\alpha$  and  $p_r^{\max}$ . (a)  $0 < \alpha \leq p_r^{\max}$ . (b)  $0 < p_r^{\max} \leq \alpha$ .

2) *Case II* ( $0 < p_r^{\max} \leq \alpha$ ): As shown in Fig. 2(b),  $\min\{f_1(p_r), f_2(p_r)\}$  can be written as

$$\min\{f_1(p_r), f_2(p_r)\} = f_2(p_r) \quad \text{for } 0 < p_r \leq p_r^{\max} \quad (18)$$

According to (18),  $\min\{f_1(p_r), f_2(p_r)\}$  is strictly increasing on  $(0, p_r^{\max}]$ . Therefore,  $p_r$  for maximizing  $\min\{f_1(p_r), f_2(p_r)\}$  is  $p_r^{\max}$ . From the two cases, the optimal  $p_r^*$  is given by

$$p_r^* = \begin{cases} \alpha, & \text{for } 0 < \alpha \leq p_r^{\max} \\ p_r^{\max}, & \text{for } 0 < p_r^{\max} \leq \alpha. \end{cases} \quad (19)$$

As mentioned, the optimal  $p_r^{*(t)}$  is  $p_r^{\max}$ . Therefore, the optimal relay transmit power in each time slot is given by

$$p_r^{*(t)} = \begin{cases} \min(\alpha, p_r^{\max}), & \text{for } 1 \leq t \leq T-1 \\ p_r^{\max}, & \text{for } t = T. \end{cases} \quad (20)$$

#### IV. PROPOSED RELAY POWER CONTROL SCHEME OVER TIME-VARYING CHANNELS

This section considers a different scenario in which the CSI in each time slot is variant. In time slot  $t$ , the relay knows the CSI in time slot  $t$ . Due to the causality, it is impossible to consider the achievable rate after the  $(t+1)$ th time slot in time slot  $t$  over time-varying channels. Therefore, we formulate the problem for finding  $p_r^{(t)}$  that maximizes the weighted sum rate of  $R^{(t)} = \min(R_{sr}^{(t-1)}, R_{rd}^{(t)})$  and  $R_{sr}^{(t)}$  in each time slot. The optimization problem that maximizes  $R^{(t)}(p_r^{(t)}) + \lambda R_{sr}^{(t)}(p_r^{(t)})$  for  $\lambda \geq 0$  can be written as

$$(P2) \quad \text{maximize}_{p_r^{(t)}} R^{(t)}(p_r^{(t)}) + \lambda R_{sr}^{(t)}(p_r^{(t)}) \quad (21a)$$

$$\text{subject to } 0 < p_r^{(t)} \leq p_r^{\max}. \quad (21b)$$

It is difficult to solve the problem (P2) because it is nonconvex problem. As mentioned,  $R^{(t+1)}$  is determined by  $R_{rd}^{(t+1)}$  as well as  $R_{sr}^{(t)}$ . Since  $R_{rd}^{(t+1)}$  cannot be considered in the  $t$ th time slot, we maximize  $R^{(t)}(p_r^{(t)})$  and then  $R_{sr}^{(t)}(p_r^{(t)})$  by setting  $\lambda$  to very close to zero. When  $\lambda$  is very close to zero, the problem (P2) can be changed to the problem for maximizing  $R_{sr}^{(t)}(p_r^{(t)})$  among  $p_r^{(t)}$  values that maximize  $R^{(t)}(p_r^{(t)})$ . The reformulated problem

can be written as

$$(\mathbf{P3}) \quad \underset{p_r^{(t)}}{\text{maximize}} \quad R_{sr}^{(t)}(p_r^{(t)}) \quad (22a)$$

$$\text{subject to } p_r^{(t)} \in \{\bar{p}_r^{(t)} | \bar{p}_r^{(t)} = \arg \max_{0 < p_r^{(t)} \leq p_r^{\max}} R^{(t)}(p_r^{(t)})\} \quad (22b)$$

The set  $\mathcal{A}^{(t)} \triangleq \{\bar{p}_r^{(t)} | \bar{p}_r^{(t)} = \arg \max_{0 < p_r^{(t)} \leq p_r^{\max}} R^{(t)}(p_r^{(t)})\}$  is the set of  $p_r^{(t)}$  that maximizes  $R^{(t)}(p_r^{(t)})$  subject to the constraint  $0 < p_r^{(t)} \leq p_r^{\max}$ . Also,  $R^{(t)}(p_r^{(t)})$  is given by

$$R^{(t)}(p_r^{(t)}) = \min\{\log_2(1 + c_{sr}^{(t-1)}), \log_2(1 + f_{rd}^{(t)}(p_r^{(t)}))\} \quad (23)$$

where  $c_{sr}^{(t-1)}$  and  $f_{rd}^{(t)}(p_r^{(t)})$  are given by

$$\begin{aligned} c_{sr}^{(0)} &= \frac{p_s^{(0)} |h_{sr}^{(0)}|^2}{\sigma_{n,r}^2} \\ c_{sr}^{(t-1)} &= \frac{p_s^{(t-1)} |h_{sr}^{(t-1)}|^2}{\sigma_{n,r}^2 + \sigma_e^2 p_r^{(t-1)}}, \quad \text{for } t \geq 2 \\ f_{rd}^{(t)}(p_r^{(t)}) &= \frac{p_r^{(t)} |h_{rd}^{(t)}|^2}{\sigma_{n,d}^2}, \quad \text{for } t \geq 1. \end{aligned} \quad (24)$$

First, we find the set  $\mathcal{A}^{(t)}$ . The optimization problem for obtaining the set  $\mathcal{A}^{(t)}$  can be written as

$$(\mathbf{P4.1}) \quad p_r^{*(t)} = \arg \max_{p_r^{(t)}} R^{(t)}(p_r^{(t)}) \quad (25a)$$

$$\text{subject to } 0 < p_r^{(t)} \leq p_r^{\max}. \quad (25b)$$

Since logarithm is a monotonically increasing function, the problem (P4.1) can be rewritten as

$$(\mathbf{P4.2}) \quad \underset{p_r^{(t)}}{\text{maximize}} \quad \min\{c_{sr}^{(t-1)}, f_{rd}^{(t)}(p_r^{(t)})\} \quad (26a)$$

$$\text{subject to } 0 < p_r^{(t)} \leq p_r^{\max}. \quad (26b)$$

From (24), we can know that  $c_{sr}^{(t-1)}$  is constant and  $f_{rd}^{(t)}(p_r^{(t)})$  increases as  $p_r^{(t)}$  increases. Let  $\beta^{(t)}$  denote  $p_r^{(t)}$  for satisfying  $c_{sr}^{(t-1)} = f_{rd}^{(t)}(p_r^{(t)})$ , and it is given by

$$\begin{aligned} \beta^{(0)} &= \frac{p_s^{(0)} \sigma_{n,d}^2 |h_{sr}^{(0)}|^2}{\sigma_{n,r}^2 |h_{rd}^{(0)}|^2} \\ \beta^{(t-1)} &= \frac{p_s^{(t-1)} \sigma_{n,d}^2 |h_{sr}^{(t-1)}|^2}{(\sigma_{n,r}^2 + \sigma_e^2 p_r^{(t-1)}) |h_{rd}^{(t-1)}|^2}, \quad \text{for } t \geq 2. \end{aligned} \quad (27)$$

The two cases are considered depending on the relation between  $\beta^{(t)}$  and  $p_r^{\max}$ . Fig. 3 shows possible cases in the  $t$ th time slot. We determine  $p_r^{(t)}$  for maximizing  $R^{(t)}$ .

1) *Case I* ( $0 < \beta^{(t)} \leq p_r^{\max}$ ): This case considers  $0 < \beta^{(t)} \leq p_r^{\max}$ . As shown in Fig. 3(a),  $R^{(t)}$  can be written as

$$R^{(t)} = \begin{cases} f_{rd}^{(t)}(p_r^{(t)}), & \text{for } 0 < p_r^{(t)} \leq \beta^{(t)} \\ c_{sr}^{(t-1)}, & \text{for } \beta^{(t)} \leq p_r^{(t)} \leq p_r^{\max}. \end{cases} \quad (28)$$

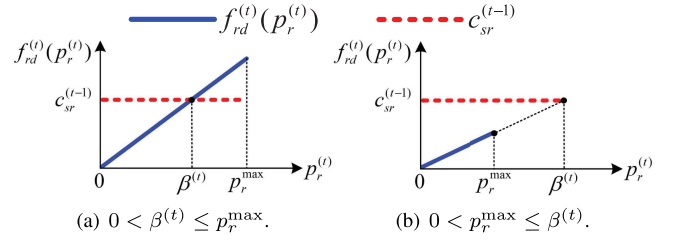


Fig. 3. Possible cases depending on the relation between  $\beta^{(t)}$  and  $p_r^{\max}$  in time slot  $t$ . (a)  $0 < \beta^{(t)} \leq p_r^{\max}$ . (b)  $0 < p_r^{\max} \leq \beta^{(t)}$ .

$R^{(t)}$  is a strictly increasing function with respect to  $p_r^{(t)}$  on  $(0, \beta^{(t)})$  and constant on  $[\beta^{(t)}, p_r^{\max}]$ . Therefore,  $p_r^{(t)}$  for maximizing  $R^{(t)}$  in the  $t$ th time slot is  $[\beta^{(t)}, p_r^{\max}]$ .

2) *Case II* ( $0 < p_r^{\max} \leq \beta^{(t)}$ ): This case considers  $0 < p_r^{\max} \leq \beta^{(t)}$ . As shown in Fig. 3(b),  $R^{(t)}$  can be written as

$$R^{(t)} = f_{rd}^{(t)}(p_r^{(t)}), \quad \text{for } 0 < p_r^{(t)} \leq p_r^{\max}. \quad (29)$$

$R^{(t)}$  is a strictly increasing function with respect to  $p_r^{(t)}$  on  $(0, p_r^{\max}]$ . Therefore,  $p_r^{(t)}$  for maximizing  $R^{(t)}$  is  $p_r^{\max}$ .

From the two cases,  $\bar{p}_r^{(t)}$  is given by

$$\bar{p}_r^{(t)} = \begin{cases} [\beta^{(t)}, p_r^{\max}], & \text{for } 0 < \beta^{(t)} \leq p_r^{\max} \\ p_r^{\max}, & \text{for } 0 < p_r^{\max} \leq \beta^{(t)}. \end{cases} \quad (30)$$

Hence, the set  $\mathcal{A}^{(t)}$  is  $\{\bar{p}_r^{(t)} | \min(\beta^{(t)}, p_r^{\max}) \leq p_r^{(t)} \leq p_r^{\max}\}$ .

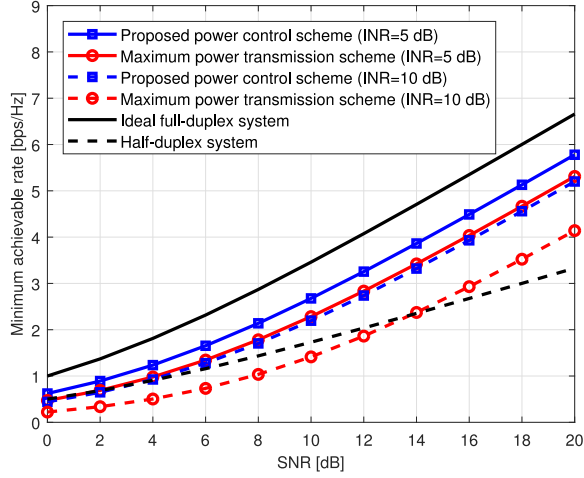
In problem (P3), since  $R_{sr}^{(t)}$  decreases as  $p_r^{(t)}$  increases,  $p_r^{*(t)}$  for maximizing  $R_{sr}^{(t)}$  should be the smallest of the set  $\mathcal{A}^{(t)}$ . Therefore,  $p_r^{*(t)}$  is given by

$$p_r^{*(t)} = \begin{cases} \beta^{(t)}, & \text{for } 0 < \beta^{(t)} \leq p_r^{\max} \\ p_r^{\max}, & \text{for } 0 < p_r^{\max} \leq \beta^{(t)}. \end{cases} \quad (31)$$

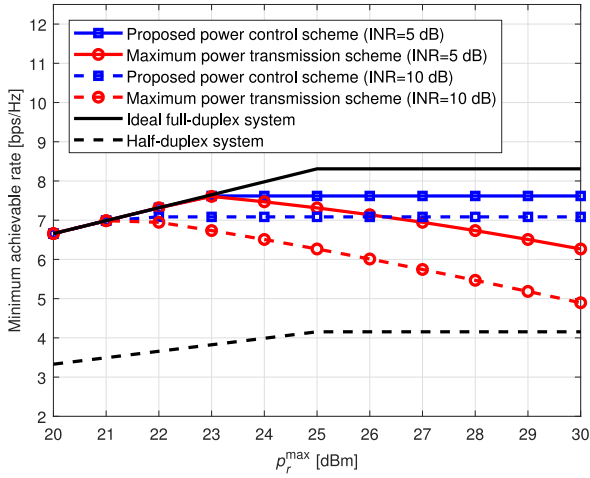
## V. NUMERICAL RESULTS

In this section, we numerically investigate the performances of the proposed relay power control schemes over static and time-varying channels. We compare the proposed relay power control schemes with the maximum power transmission scheme. In the maximum power transmission scheme, the relay transmits the decoded signal using the maximum transmit power in each time slot. It is assumed that the transmit power in each time slot at the source is the same, i.e.,  $p_s^{(1)} = p_s^{(2)} = \dots = p_s$ . Also, we assume that the noise variance at each node is the same, i.e.,  $\sigma_{n,d}^2 = \sigma_{n,r}^2 = \sigma_n^2$ . The signal-to-noise ratio (SNR) and interference-to-noise ratio (INR) are defined as  $p_s/\sigma_n^2$  and  $\sigma_e^2/\sigma_n^2$ , respectively.

Fig. 4 plots the performance for the proposed power control scheme over static channels when  $|h_{sr}^{(t)}|^2 = |h_{rd}^{(t+1)}|^2 = 1$  for  $t \geq 0$ . Fig. 4(a) shows the minimum achievable rate versus SNR for different INRs when  $p_s = 27$  dBm and  $p_r^{\max} = 27$  dBm. It is seen that the minimum achievable rate of the proposed power control scheme is greater than that of the maximum power transmission scheme. The ideal FD system refers to the



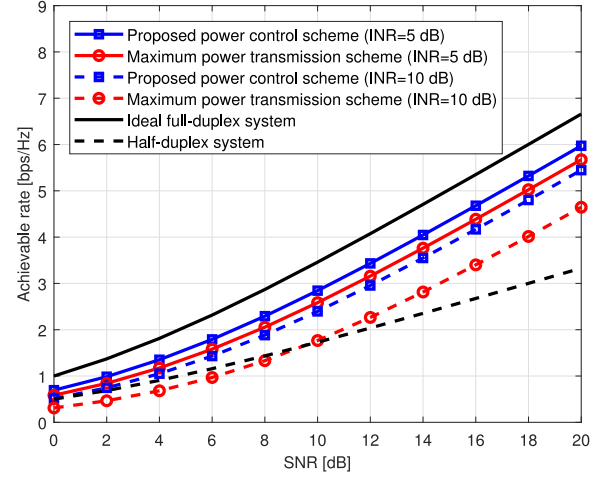
(a)



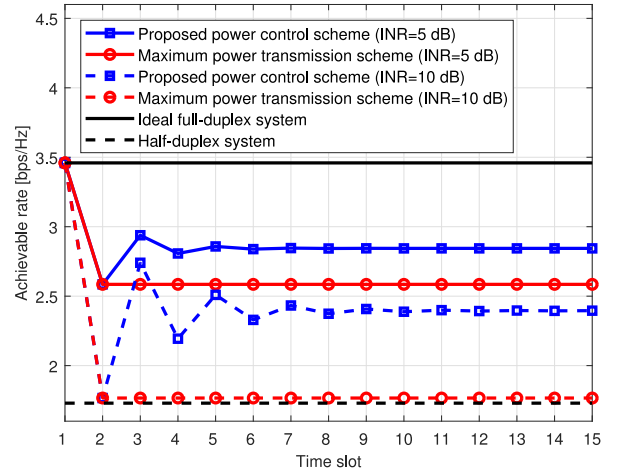
(b)

Fig. 4. Performance for the proposed power control and maximum power transmission schemes over static channels. (a) Minimum achievable rate versus SNR for different INRs. (b) Minimum achievable rate versus maximum transmit power at the relay for different INRs.

proposed scheme when  $\text{INR} = 0$ , i.e., there is no residual SI. Therefore, the minimum achievable rate of the ideal FD system is superior to those of the other schemes. Fig. 4(b) shows the minimum achievable rate versus  $p_r^{\max}$  for different INRs when  $\text{SNR} = 25$  dB and  $p_s = 25$  dBm. The minimum achievable rate of the proposed power control scheme is greater than that of the maximum power transmission scheme. It can be verified that when  $\text{INR} = 5$  dB, the optimal  $p_r^{*(t)}$  is  $p_r^{\max}$  for  $20 \leq p_r^{\max} \leq 23$  dBm and the optimal  $p_r^{*(t)}$  is  $\alpha$  for  $23 \leq p_r^{\max} \leq 30$  dBm. Therefore, the minimum achievable rate of the proposed power control scheme and that of the maximum power transmission scheme are the same for  $20 \leq p_r^{\max} \leq 23$  dBm. In addition, the minimum achievable rate of the maximum power transmission scheme increases for  $20 \leq p_r^{\max} \leq 23$  dBm and decreases for  $23 \leq p_r^{\max} \leq 30$  dBm. Similarly, when  $\text{INR} = 10$  dB, the minimum achievable rate of the maximum power transmission scheme increases for  $20 \leq p_r^{\max} \leq 21$  dBm and decreases for  $21 \leq p_r^{\max} \leq 30$  dBm.



(a)



(b)

Fig. 5. Performance for the proposed power control and maximum power transmission schemes over time-varying channels. (a) Achievable rate versus SNR for different INRs in time slot 15. (b) Achievable rate versus time slot for different INRs.

Fig. 5 plots the achievable rates for the proposed power control scheme over time-varying channels when  $p_s = 25$  dBm,  $p_r^{\max} = 25$  dBm,  $|h_{sr}^{(t)}|^2 = 1$ , and  $|h_{rd}^{(t+1)}|^2 = 1$  for  $t \geq 0$ . Fig. 5(a) shows the achievable rate versus SNR for different INRs up to the 15th time slot. The achievable rate of the proposed power control scheme is greater than that of the maximum power transmission scheme regardless of SNR and INR values. Fig. 5(b) shows the achievable rate versus time slot for different INRs when  $\text{SNR} = 10$  dB. It can be seen that the achievable rate of the proposed power control scheme is greater than that of the maximum power transmission scheme in each time slot. It can also be seen that the achievable rate of the proposed power control scheme changes from a time slot to another. This is because relay transmit power in each time slot is updated by its transmit power in the previous time slots in order to maximize the achievable rate. Therefore, the influence of the relay transmit power in the

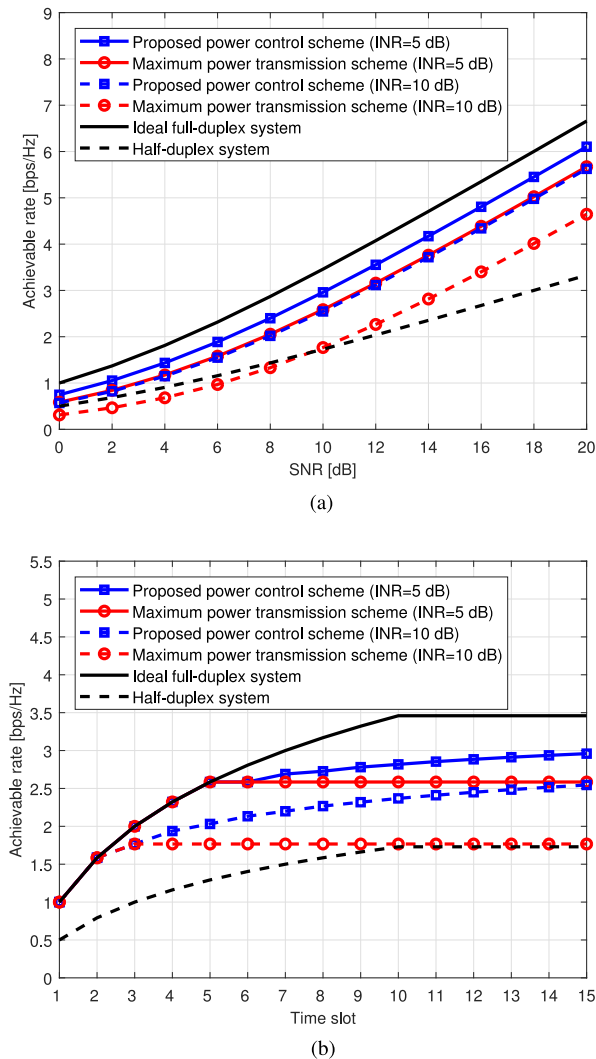


Fig. 6. Performance of the proposed power control and maximum power transmission schemes over time-varying channels when channel gains are changed. (a) Achievable rate versus SNR for different INRs in time slot 15. (b) Achievable rate versus time slot for different INRs.

latest time slot on the achievable rate decreases as the number of time slots increases. Hence, the achievable rate becomes constant as the number of time slots goes to infinity. Since only the source transmits a signal in time slot 0, the achievable rate in time slot 1 is not affected by residual SI. Therefore, the achievable rate of all schemes is the same in time slot 1.

Fig. 6 plots the achievable rates for the proposed power control scheme over time-varying channels when  $p_s = 25$  dBm,  $p_r^{\max} = 25$  dBm,  $|h_{sr}^{(t)}|^2 = 1$ , and  $|h_{rd}^{(t+1)}|^2 = 0.1(t+1)$  for  $t \geq 0$ . Fig. 6(a) shows the achievable rate versus SNR for different INRs up to the 15th time slot. Even if the channel gain changes, it is observed that the achievable rate of the proposed power control scheme is greater than that of the maximum power transmission scheme. Fig. 6(b) shows the achievable rate versus time slot for different INRs when SNR = 10 dB. In addition, a general trend seen in all achievable rate plots in Figs. 4–6 is that the minimum achievable rate increases as the SNR increases or the INR decreases.

## VI. CONCLUSION

In this article, we proposed two power control schemes at the relay for one-way FD decode-and-forward relay networks over static and time-varying channels. The relay transmit power in each time slot was updated by the transmit power in the previous time slots. Even if the channel gain, channel estimation error variance, and noise variance are invariant, the transmit power at the relay should be different in each time slot to maximize the achievable rate. Since the proposed power control scheme has a closed-form solution, its complexity is very low.

## REFERENCES

- [1] A. Sabharwal, P. Schniter, D. Guo, D. W. Bliss, S. Rangarajan, and R. Wichman, "In-band full-duplex wireless: Challenges and opportunities," *IEEE J. Sel. Areas Commun.*, vol. 32, no. 9, pp. 1637–1652, Sep. 2014.
- [2] G. Liu, F. R. Yu, H. Ji, V. C. M. Leung, and X. Li, "In-band full-duplex relaying: A survey, research issues and challenges," *IEEE Commun. Surv. Tut.*, vol. 17, no. 2, pp. 500–524, Apr.–Jun. 2015.
- [3] B. P. Day, A. R. Margetts, D. W. Bliss, and P. Schniter, "Full-duplex MIMO relaying: Achievable rates under limited dynamic range," *IEEE J. Sel. Areas Commun.*, vol. 30, no. 8, pp. 1541–1553, Sep. 2012.
- [4] M. Vaezi, H. Inaltekin, W. Shin, H. V. Poor, and J. Zhang, "Social-aware user cooperation in full-duplex and half-duplex multi-antenna systems," *IEEE Trans. Commun.*, vol. 66, no. 8, pp. 3309–3321, Aug. 2018.
- [5] K. E. Kolodziej, B. T. Perry, and J. S. Herd, "In-band full-duplex technology: Techniques and systems survey," *IEEE Trans. Microw. Theory Techn.*, vol. 67, no. 7, pp. 3025–3041, Jul. 2019.
- [6] G. Zheng, "Joint beamforming optimization and power control for full-duplex MIMO two-way relay channel," *IEEE Trans. Signal Process.*, vol. 63, no. 3, pp. 555–566, Feb. 2015.
- [7] Y. Shim, W. Choi, and H. Park, "Beamforming design for full-duplex two-way amplify-and-forward MIMO relay," *IEEE Trans. Wireless Commun.*, vol. 15, no. 10, pp. 6705–6715, Oct. 2016.
- [8] Y. Zeng and R. Zhang, "Full-duplex wireless-powered relay with self-energy recycling," *IEEE Wireless Commun. Lett.*, vol. 4, no. 2, pp. 201–204, Apr. 2015.
- [9] H. Liu, K. J. Kim, K. S. Kwak, and H. V. Poor, "Power splitting-based SWIPT with decode-and-forward full-duplex relaying," *IEEE Trans. Wireless Commun.*, vol. 15, no. 11, pp. 7561–7577, Nov. 2016.
- [10] D. Wang, R. Zhang, X. Cheng, and L. Yang, "Capacity-enhancing full-duplex relay networks based on power splitting (PS-)SWIPT," *IEEE Trans. Veh. Technol.*, vol. 66, no. 6, pp. 5445–5450, Jun. 2017.
- [11] C. Guo, L. Zhao, C. Feng, Z. Ding, and H.-H. Chen, "Energy harvesting enabled NOMA systems with full-duplex relaying," *IEEE Trans. Veh. Technol.*, vol. 68, no. 7, pp. 7179–7183, Jul. 2019.
- [12] H. Dun, F. Ye, and Y. Li, "Transmission power adaption for full-duplex relay-aided device-to-device communication," *MDPI Sens.*, vol. 9, no. 3, pp. 1–13, Mar. 2017.
- [13] J.-W. Li and C. Lin, "On the optimal power allocation for two-way full-duplex amplify-and-forward relay networks," *IEEE Trans. Signal Process.*, vol. 65, no. 21, pp. 5702–5715, Nov. 2017.
- [14] Y. Li, N. Li, M. Peng, and W. Wang, "Relay power control for two-way full-duplex amplify-and-forward relay networks," *IEEE Signal Process. Lett.*, vol. 23, no. 2, pp. 292–296, Feb. 2016.
- [15] X. Huang, K. Yang, F. Wu, and S. Leng, "Power control for full-duplex relay-enhanced cellular networks with QoS guarantees," *IEEE Access*, vol. 5, pp. 4859–4869, 2017.
- [16] M. M. Razlighi and N. Zlatanov, "Buffer-aided relaying for the two-hop full-duplex relay channel with self-interference," *IEEE Trans. Wireless Commun.*, vol. 17, no. 1, pp. 477–491, Jan. 2018.
- [17] K. T. Phan and T. Le-Ngoc, "Power allocation for buffer-aided full-duplex relaying with imperfect self-interference cancellation and statistical delay constraint," *IEEE Access*, vol. 4, pp. 3961–3974, 2016.
- [18] N. Zlatanov, E. Sippel, V. Jamali, and R. Schober, "Capacity of the Gaussian two-hop full-duplex relay channel with residual self-interference," *IEEE Trans. Commun.*, vol. 65, no. 3, pp. 1005–1021, Mar. 2017.
- [19] J. N. Laneman, D. N. C. Tse, and G. W. Wornell, "Cooperative diversity in wireless networks: Efficient protocols and outage behaviors," *IEEE Trans. Inf. Theory*, vol. 50, no. 12, pp. 3062–3080, Dec. 2004.



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